

BUILD OR BUY PC NEWS

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—Extended Data Out Random Access Memory—EDO RAM—

*Increase performance over standard
DRAM by 20%.*

EDO RAM, a type of DRAM which supports a “fast paging mode”, allowing for faster access. EDO RAM is primarily used with Intel’s Pentium processors and Intel’s “Triton” chipset (*see chipset below*). In early 1995, EDO RAM was available for computers from Micron, Gateway, and Intel.

chip set

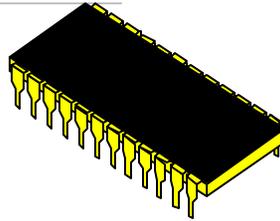
A collection of integrated circuits that are designed to be used together for some specific purpose. Eg. control circuitry in an IBM PC.

Burst EDO DRAMs: The performance option worth watching!

*Designed for a new level of performance without
paying the price.*

Burst Extended Data-out (EDO) is a cost effective, high-speed access option to standard DRAMs that can increase the page mode rate to 66 MHz bus speeds—without requiring changes to system architecture. They use standard DRAM packaging and need only minor timing modifications.

The concept behind the Burst EDO memory is to carry the EDO DRAM, in its existing package, to a higher performance level. EDO is a simple modification to a Fast Page Mode (FPM) DRAM that allows the EDO Page Mode rate to be improved from 25 MHz up to 50 MHz.



Burst EDO is a further improvement to EDO that increases the page mode rate to 66 MHz and beyond. This speed increase is achieved by implementing a processor-compatible, four-cycle burst count on-chip.

A key advantage of Burst EDO over more complicated high-speed DRAM alternatives is the ease of implementation. Burst EDO DRAMs use packages and pinouts identical to standard EDO DRAMs. New bus architecture, a separate die, or dedicated test and production equipment are not necessary. Chipset vendors will make only minimal changes to their designs to support Burst EDO.

Highlights of the new Burst EDO...

- w Zero wait state bursts at 66 MHz
- w Multiple DRAM vendor support
- w Multiple chipset support
- w Same pinout and package as EDO DRAMs
- w Cost effective performance - on the same die as industry standard FPM DRAMs and high volume EDO DRAMs
- w 3.3V operation with 5V tolerant I/O for 5V system or 3.3V system operation
- w Supports both Intel burst order and linear burst order
- w Available on industry standard modules: 72-pin SIMM (FPM or EDO) and 168 pin DIMM (FPM or EDO)

Pentium Chipsets Supporting EDO DRAMs

Currently available Chipsets

Intel Triton, OPTI Viper, VLSI Wildcat chipsets.
Other Chipsets will be made available shortly.
Expect 486 CPU support very soon!

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Sources: Intel & Micron Technology